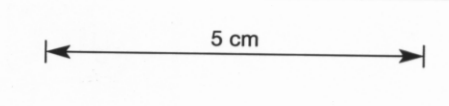
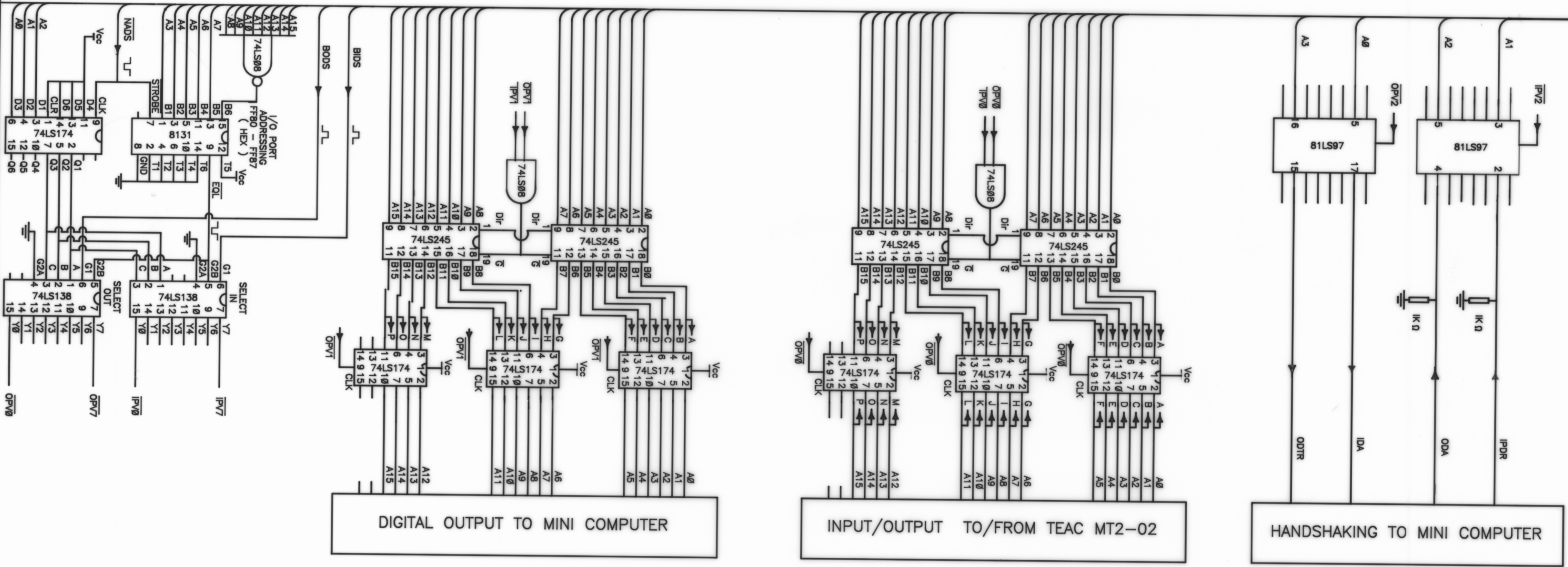


WIRE LOGGER INTERFACE FOR MINICOMPUTER CIRCUIT DIAGRAM
 FIGURE No. U/R 89/20 AUTHOR: R.J. SEDGMAN
 TASMANIA DEPARTMENT OF MINES, DRAFTSPERSON: ASM 09/10/89 **5299C**



PACE INTERNAL DATA BUS



DIGITAL OUTPUT TO MINI COMPUTER

INPUT/OUTPUT TO/FROM TEAC MT2-02

HANDSHAKING TO MINI COMPUTER

DIGITAL INPUT FROM MINI COMPUTER

SUPPLY PINS

IC	Vcc	GND
74LS30	14	7
74LS174	16	8
74LS138	16	8
74LS08	14	7
74LS245	20	10
81LS97	20	10
8131	16	8

